


PA-~~ISC~~




Electronic Information Disclosure Statement

Integrated Circuit Package and Printed Circuit Board Arrangement

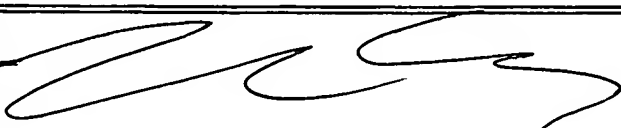
Application: 
10/065016
Confirmation: 5731
Applicant(s): Olivier Boireau
Docket Number: 71522-2
Group Art Unit:
Examiner: Lourdes C. Cruz
search string: (4994902).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
LC	P01	4994902	1991-02-19		Okahashi et al.		

Signature

Examiner Name	Date
	6/10/03

Search Notes



Application No.

10/065,016

Examiner

Lourdes Cruz

Applicant(s)

BOIREAU, OLIVIER

Art Unit

2827

SEARCHED

Class	Subclass	Date	Examiner
257	734	6/03	LC
	786		
	090		
174	260		

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner
as above	as above	6/03	LC

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	DATE	EXMR
East	6/03	LC